

[06 ELECTRICAL & ELECTRONIC]
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : MAY-2014

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY MAX. 100 MIN. 040	SESSIONAL 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	ORAL 50 OR 25 20 OR 10	REMARKS MAX.850 MIN.340
PAPER DESCRIPTION											
0010	200703655	(E)	N	M	CHAGAS PEREIRA JASON MARK STEFAN						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	048	010	058E		030+	
					ELECTRICAL POWER SYSTEM II	038	\$2 011	049+	\$2		
					ADVANCED CONTROLLED DRIVES	036	\$4 010	046+	\$4		
					NEURAL NETWORKS & FUZZY LOGIC	020	012	032F		030+	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	044	007 020+	051E		033+ 038+	
0011	201006312	(E)	N	M	SAWAL SAGAR NAMDEV						
					VLSI CIRCUIT DESIGN	062	010	072P		037+	
					ELECTRICAL POWER SYSTEM II	037	\$3 009	046+	\$4		
					ADVANCED CONTROLLED DRIVES	047	010	057+			
					NEURAL NETWORKS & FUZZY LOGIC	036	\$4 010	046+	\$4	044+	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	041	013 023+	054+		026+ 045+	450 \$08 P
0040	201008366	(E)	N	M	BARVE ANKIT RAMESH						
					VLSI CIRCUIT DESIGN	060	007	067P		035+	
					ELECTRICAL POWER SYSTEM II	053	013	066+			
					ADVANCED CONTROLLED DRIVES	052	010	062+			
					NEURAL NETWORKS & FUZZY LOGIC	056	011	067+		030+	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	071	013 023+	084+		038+ 047+	519 P
0041	200907816	(E)	N	M	CHARI SAGAR DATTA						
					VLSI CIRCUIT DESIGN	042	010	052+		036+	
					ELECTRICAL POWER SYSTEM II	054	010	064+			
					ADVANCED CONTROLLED DRIVES	050	010	060+			
					NEURAL NETWORKS & FUZZY LOGIC	035	001	036F		025+	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	046	008 020+	054+		029+ 045+	421 F
0042	201008375	(E)	N	M	DE ARAUJO BEN ALAIN JOSE						
					VLSI CIRCUIT DESIGN	055	013	068+		042+	
					ELECTRICAL POWER SYSTEM II	064	012	076+			
					ADVANCED CONTROLLED DRIVES	048	010	058+			
					NEURAL NETWORKS & FUZZY LOGIC	036	\$4 014	050+	\$4	040+	
					FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	045	010 022+	055P		035+ 046+	492 \$04 P

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PAPER DESCRIPTION											
0043	200908072	(E)	N	M	KHAIDEM KASISH KUMAR SINGH						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	046	010	056+		035+	
					ELECTRICAL POWER SYSTEM II	052	009	061+			
					ADVANCED CONTROLLED DRIVES	054	010	064+			
					NEURAL NETWORKS & FUZZY LOGIC	038	005	043F		025+	
					FLEXIBLE AC TRANSMISSION SYSTEM	048	011	059+		025+	
					PROJECT		018+			037+	
0044	201008360	(E)	N	M	LOPES FRANCIS BOMDIN						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	035	006	041F		035+	
					ELECTRICAL POWER SYSTEM II	052	016	068+			
					ADVANCED CONTROLLED DRIVES	067	010	077+			
					NEURAL NETWORKS & FUZZY LOGIC	045	015	060+		022+	
					FLEXIBLE AC TRANSMISSION SYSTEM	056	014	070+		037+	
					PROJECT		020+			044+	
0045	200907824	(E)	N	M	MANTRI RADHAKRISHNA VIPRADAS						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
					VLSI CIRCUIT DESIGN	053	006	059P		036+	
					ELECTRICAL POWER SYSTEM II	052	004	056+			
					ADVANCED CONTROLLED DRIVES	052	010	062+			
					NEURAL NETWORKS & FUZZY LOGIC	052	000	052+		022+	
					FLEXIBLE AC TRANSMISSION SYSTEM	050	009	059+		029+	
					PROJECT		021+			046+	
0046	201104809	(E)	N	M	NAIK PRAVEEN JANARDHAN						
					VLSI CIRCUIT DESIGN	064	011	075P		035+	
					ELECTRICAL POWER SYSTEM II	046	010	056+			
					ADVANCED CONTROLLED DRIVES	057	010	067+			
					NEURAL NETWORKS & FUZZY LOGIC	043	010	053+		030+	
					FLEXIBLE AC TRANSMISSION SYSTEM	044	013	057+		032+	
					PROJECT		021+			042+	
0047	201104801	(E)	N	M	TALEKAR NIKHIL SURESH						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					VLSI CIRCUIT DESIGN	042	007	049P	\$1	037+	
					ELECTRICAL POWER SYSTEM II	066	010	076+			
					ADVANCED CONTROLLED DRIVES	065	010	075+			
					NEURAL NETWORKS & FUZZY LOGIC	045	010	055+		028+	
					FLEXIBLE AC TRANSMISSION SYSTEM	059	013	072+		033+	
					PROJECT		020+			040+	

468 P

READ BY *[Signature]*

CHECKED BY *[Signature]*

DECLARED ON 8 AUG 2014

ASST. REGISTRAR-E (PROF) *[Signature]*

CONTROLLER OF EXAMINATIONS *[Signature]*

REGISTRAR *[Signature]*